## WHAT IS CLAIMED IS:

|        | 1. An access method in a storage device comprising:   |  |  |
|--------|---|--|--|
| 2      | receiving received data in connection with a first operation on the storage                   |  |  |
| 3      | device;   |  |  |
| 1      | performing the first operation if a predetermined data sequence in the receive                |  |  |
| 5      | data is not detected; and   |  |  |
| 5      | performing a predetermined operation that is not the first operation in respon                |  |  |
| 7      | to detecting the predetermined data sequence in the received data.                            |  |  |
| ı      | 2. The method of claim 1 further comprising performing the first                              |  |  |
| )      | operation in addition to performing the predetermined operation, in response to detecting the |  |  |
| -<br>3 | predetermined data sequence in the received data.   |  |  |
|        | production and confusion in the second confusion (  |  |  |
| ĺ      | 3. The method of claim 1 wherein the predetermined operation is                               |  |  |
| 2      | performed instead of the first operation, in response to detecting the predetermined data     |  |  |
| 3      | sequence in the received data.  |  |  |
| 1      | 4. The method of claim 1 wherein the received data further includes a                         |  |  |
| 2      | second data sequence in addition to the predetermined sequence, wherein the predetermined     |  |  |
| 3      | operation is indicated by the second data sequence.   |  |  |
|        |   |  |  |
| 1      | 5. The method of claim 1 wherein performing the predetermined                                 |  |  |
| 2      | operation produces result data.   |  |  |
| 1      | 6. The method of claim 5 further comprising receiving a read operation                        |  |  |
| 2      |   |  |  |
|        |   |  |  |
| 1      | 7. The method of claim 6 wherein the step of producing result data                            |  |  |
| 2      | includes storing the result data in a memory component of the storage device at a             |  |  |
| 3      | predetermined location in the memory component, wherein the read operation includes           |  |  |
| 4      | address information indicative of the predetermined location.                                 |  |  |
| 1      | 8. The method of claim 1 wherein the first operation is a write operation,                    |  |  |
| 2      | wherein performing the first operation includes storing the received data in a memory         |  |  |
| 3      | component of the storage device.  |  |  |
|        |   |  |  |

| 1   | 9. The method of claim 8 wherein performing the predetermined                                |  |  |
|-----|--|--|--|
| 2   | operation produces result data that is stored in a data store other than the memory compone  |  |  |
| 1   | 10. The method of claim 8 wherein performing the predetermined                               |  |  |
| 2   | operation produces result data that is stored in the memory component.                       |  |  |
| 1   | 11. The method of claim 8 wherein the received data comprises an address                     |  |  |
| 2   | portion and a data portion, wherein the data portion comprises the predetermined data        |  |  |
| .3  | sequence.  |  |  |
| 1   | 12. The method of claim 11 wherein the data portion further comprises a                      |  |  |
| 2   | command identifier that identifies the predetermined operation.                              |  |  |
| 1   | 13. The method of claim 8 wherein the received data comprises an addres                      |  |  |
| 2   | portion and a data portion, wherein the address portion comprises the predetermined data     |  |  |
| 3   | sequence.  |  |  |
| - 1 | 14. The method of claim 13 wherein the data portion comprises a                              |  |  |
| 2   | command identifier that identifies the predetermined operation.                              |  |  |
| 1   | 15. A storage device having a memory component and a control                                 |  |  |
| 2   |  |  |  |
| 1   | 16. The storage device of claim 15, wherein the control component is                         |  |  |
| 2   | further configured to communicate with a computing device.                                   |  |  |
| 1   | 17. A method for operating a data storage device comprising:                                 |  |  |
| 2   | receiving first data associated with a write operation;                                      |  |  |
| 3   | if the first data includes a predetermined sequence, then performing at lea                  |  |  |
| 4   | first operation other than the write operation; and  |  |  |
| 5   | if the first data does not include the predetermined sequence, then perform                  |  |  |
| 6   |  |  |  |
| 1   | 18. The method of claim 17 wherein performing the first operation                            |  |  |
| 2   | generates result data, wherein responsive to a subsequent read operation, the result data is |  |  |
| 3   | produced as a response to the read operation.  |  |  |

| 1  | 19. The method of claim 18 further comprising storing the result data in a   |  |  |
|----|--|--|--|
| 2  | data store other than the memory.  |  |  |
| 1  | The method of claim 18 further comprising storing the result data in   |  |  |
| 2  | the memory.  |  |  |
| 1  | 21. The method of claim 20 wherein the result data is stored beginning at a  |  |  |
| 2  | predetermined location in memory and the subsequent read operation includes an address   |  |  |
| 3  | indicative of the predetermined location.  |  |  |
| 1  | 22. A method for accessing a storage device comprising:  |  |  |
| 2  | communicating a first write operation to a storage device, the first write   |  |  |
| 3  | operation having associated therewith first data comprising address data and write data,   |  |  |
| 4  |  |  |  |
| 5  | data;  |  |  |
| 6  | communicating a second write operation to the storage device, the second   |  |  |
| 7  |  |  |  |
| 8  |  |  |  |
| 9  | operation in response to detecting the predetermined data sequence; and  |  |  |
| 0  | communicating a read operation subsequent to the second write operation,   |  |  |
| 1  | wherein the predetermined operation produces result data,  |  |  |
| 12 | wherein the storage device responds to the read operation with the result data.  |  |  |
| 1  | The method of claim 22 wherein the step of communicating a first   |  |  |
| 2  | write operation is performed in response to performing a first write operation to a file, the  |  |  |
| 3  |  |  |  |
| 4  | and the second s |  |  |
| 5  | performed in response to performing a read operation on the file.  |  |  |
| 1  | 24. The method of claim 23 wherein the steps of performing a second  |  |  |
| 2  | write operation to the file and performing a read operation on the file are performed in   |  |  |
| 3  | response to making an API (application programmer's interface) call to perform the   |  |  |
| 4  | predetermined operation in the storage device.   |  |  |

| 1 | 25. The method  | of claim 24 wherein the steps of making an API call,   |  |  |
|---|---|--|--|--|
| 2 | performing a second write operation   | performing a second write operation to the file, and performing a read operation on the file |  |  |
| 3 | are performed by program code wh  | are performed by program code which comprise one or more program code portions of an         |  |  |
| 4 | application.  | application.   |  |  |
|   |   | California 25 calculated the star of a communication of first                                |  |  |
| 1 |   | of claim 25 wherein the step of communicating a first  |  |  |
| 2 | write operation, communicating a second write operation, and communicating a read                 |  |  |  |
| 3 |   | operation are performed by program code which comprise one or more program code              |  |  |
| 4 | portions of an OS (operating system).   |  |  |  |
| 1 | 27. The method  | of claim 22 wherein the storage device is configured to                                      |  |  |
| 2 |   |  |  |  |
|   |   |  |  |  |
| 1 | 28. The method  | of claim 27 wherein the second data further comprises a                                      |  |  |
| 2 | command data sequence that is indicative of the predetermined operation.                          |  |  |  |
| 1 | 29. The method  | of claim 27 wherein the second data further comprises  |  |  |
| 2 |   | nined data sequence constitutes the address data.  |  |  |
| 2 | address data, wherein the production  | inned data sequence constitutes the west-spectrum.   |  |  |
| 1 | The method  | of claim 22 wherein the result data is stored in a data store                                |  |  |
| 2 | other than the memory.  |  |  |  |
|   | The method  | of claim 22 wherein the result data is stored beginning at a                                 |  |  |
| 1 |   |  |  |  |
| 2 | 2 predetermined location in the mem   | ory.   |  |  |
| 1 | 1 32. A method for  | or accessing a data storage device comprising:   |  |  |
| 2 | communicating an  | ndication to the data storage device to perform a first                                      |  |  |
| 3 | operation, the first operation being one of a plurality of first device operations;               |  |  |  |
| 4 | •   | t data to the data storage device, the first data being                                      |  |  |
| 5 | associated with the first operation;  |  |  |  |
| 6 | determining whether to perform at least a second operation based on data                          |  |  |  |
| 7 | contained in the first data, the second operation being exclusive of the plurality of first devi- |  |  |  |
| 8 |   |  |  |  |
| • |   | ·  |  |  |
| 1 |   | of claim 32 wherein the data storage device has a  |  |  |
| 2 | 2 corresponding command set assoc   | ated with the first device operations, wherein the second                                    |  |  |

| 3  | operation is not associated with any commands in the command set, wherein the step of           |  |  |
|----|---|--|--|
| 4  | communicating an indication is a step of communicating a command from the command set.          |  |  |
| 1  | 34. The method of claim 33 wherein the data storage device has only a                           |  |  |
| 2  | single corresponding command set.   |  |  |
| 1  | 35. The method of claim 32 further comprising performing the second                             |  |  |
| 2  | operation instead of the first operation if the first data contains a predetermined sequence of |  |  |
| 3  | data.   |  |  |
| 1  | 36. The method of claim 32 further comprising performing the second                             |  |  |
| 2  |   |  |  |
| 3  | predetermined sequence of data.   |  |  |
| 1  | 37. The method of claim 32 wherein the first operation is a write operation                     |  |  |
| 2  |   |  |  |
| 1  | 38. The method of claim 32 wherein the steps of communicating include                           |  |  |
| 2  | asserting signals on one or more signal lines of the data storage device.                       |  |  |
| 1  | 39. The method of claim 32 wherein the steps of communicating include                           |  |  |
| 2  | transmitting data over one or more data lines of the data storage device.                       |  |  |
| 1  | 40. A method of accessing a storage device comprising:  |  |  |
| 2  | providing a plurality of first operations in the storage device, each first                     |  |  |
| 3  | operation having an associated command, thereby defining a command set;                         |  |  |
| 4  | providing a plurality of second operations in the storage device, the comman                    |  |  |
| 5  |   |  |  |
| 6  | communicating to the storage device a command associated with one of the                        |  |  |
| 7  | first operations, including communicating data associated with the command;                     |  |  |
| 8  | detecting a predetermined data sequence in the data associated with the                         |  |  |
| 9  | command, and in response thereto, performing one of the second operations; and                  |  |  |
| 10 |   |  |  |
| 11 | sequence, then performing one of the first operations associated with the command.              |  |  |
| 1  | The method of claim 40 wherein performing one of the second                                     |  |  |

operations produces result data.

| ı   | 42. The method of claim 40 wherein the data associated with the command                    |  |  |
|-----|--|--|--|
| 2   | includes data indicative of one of the second operations.                                  |  |  |
| 1   | 43. The method of claim 42 wherein the command is a write command.                         |  |  |
| 1   | 44. The method of claim 42 wherein one of the first operations is                          |  |  |
| 2   | associated with the command, the method further comprising performing the associated first |  |  |
| 3   | command in addition to performing one of the second operations.                            |  |  |
| 1   | 45. The method of claim 40 further comprising communicating a                              |  |  |
| 2   | subsequent command to the storage device, the subsequent command having data associat      |  |  |
| 3   | therewith, wherein the data is indicative of one of the second operations.                 |  |  |
| 1   | 46. The method of claim 40 wherein performing the second operation                         |  |  |
| 2   | produces result data, the method further comprising communicating a subsequent command     |  |  |
| 3   | that is indicative of a read operation, detecting the subsequent command and in response   |  |  |
| 4   | thereto responding with the result data.   |  |  |
| 1   | 47. The method of claim 46 wherein the steps of communicating are                          |  |  |
| 2   | performed by executing first computer program code.  |  |  |
| 1   | 48. The method of claim 47 wherein executing the first program code is                     |  |  |
| 2 - | performed in response to executing second program code.                                    |  |  |
| 1   | 49. The method of claim 48 wherein the first program code is a constituer                  |  |  |
| 2   | part of an operating system, wherein the second program code is a constituent part of an   |  |  |
| 3   | application program.   |  |  |
| 1   | 50. A storage device comprising:   |  |  |
| 2   | a memory component having a corresponding command set;                                     |  |  |
| 3   | a control component operatively coupled to the memory component for                        |  |  |
| 4   | writing data to the memory component and for reading data from the memory component;       |  |  |
| 5   | and  |  |  |
| 6   | and interface operably coupled to the control component and configured for                 |  |  |
| 7   | communication with a host device,  |  |  |

the control component configured to perform a plurality of device operations and a plurality of extended operations, wherein each of the device operations is associated with a command in the command set,

the control component further configured to detect commands communicated from a host device wherein the device operation associated with a communicated command can be performed,

the control component further configured to detect a predetermined sequence of data in first data associated with a first command from the command set, and to perform one of the extended operations in response to detecting the predetermined sequence of data.

- 51. The device of claim 50 wherein the extended operations do not have associated commands in the command set.
- 52. The device of claim 50 further comprising a data store, wherein at least one of the extended operations generates result data that can then be stored in the memory component.
- 53. The device of claim 50 wherein at least one of the extended operations generates result data, wherein the control component is further configured to store the result data beginning at a predetermined location in the memory.
- 54. The device of claim 50 wherein the first command is a write command and the first data comprises an address portion and a data portion.
- 55. The device of claim 54 wherein the data portion comprises the predetermined sequence of data and an extended command specifier, wherein the control component is further configured to perform one of the extended operations based on the extended command specifier.
- 56. The device of claim 54 wherein the address portion comprises the predetermined sequence of data and the data portion comprises an extended command specifier, wherein the control component is further configured to perform one of the extended operations based on the extended command specifier.

57. The device of claim 50 wherein the control component is further 2 configured to perform a device operation corresponding to the first command in addition to performing the extended operation in response to detecting the predetermined sequence of 3 4 data.

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- 1 58. The device of claim 50 the extended operation is performed instead of performing a device operation corresponding to the first command in response to detecting 2 the predetermined sequence of data. 3
  - The device of claim 50 wherein the control component comprises a 59. controller and one or more processing units, the one or more processing units configured to perform the extended operations, the controller comprising logic to detect the predetermined sequence of data, the controller operatively coupled to operate the one or more processing units to perform an extended operation in response to detecting the predetermined sequence of data.
- A host device configured to communicate with the storage device of 60. 1 2 claim 50 comprising first software for communicating one or more of the commands to the storage device. 3
  - The host device of claim 60 further comprising second software for 61. performing file input and file output operations, wherein the first software communicates one or more of the commands to the storage device in response to the file input and file output operations.
  - 62. A method for accessing a memory device, the memory device configured to perform a plurality of first operations and a plurality of second operations, each of the first operations having an associated command, the memory device further configured to respond to one of the commands communicated thereto by performing its associated first operation, the method comprising:
  - detecting a predetermined sequence of two or more commands communicated to the storage device;
- if the predetermined sequence of two or more commands is detected, then performing one of the second operations. 9

- 1 63. The method of claim 62 wherein the second operation that is 2 performed is based on the sequence of commands comprising the predetermined sequence.
- 1 64. The method of claim 62 wherein if a second predetermined sequence 2 of two or more commands is detected then performing another one of the second operations.